

Notice of References Cited	Application/Control No. 10/763,353		Applicant(s)/Patent Under Reexamination HOFFMAN ET AL.	
	Examiner Johannes P. Mondt		Art Unit 2826	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,184,946 B1	02-2001	Ando et al.	349/42
	B	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, "Silicon Processing for the VLSI Era", Volume 3 - the submicron MOSFET, pages 136-137, Lattice Press, Sunset Beach, CA (1995) (ISBN: 0-961672-5-3).
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.